COMMUNICATION PORTS IN A DATA DRIVEN ARCHITECTURE

Abstract

In an embodiment, an apparatus includes a first processor having a first
processor element and at least one input/output (I/O) port within a first port ring.
The apparatus also includes a second processor having a second processor element
and at least one I/O port within a second port ring, wherein the second processor is
coupled to the first processor through at least one I/O port of a third port ring of a
third processor.

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